

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S21 5	201	S211 and S214	US-PGPUB; USPAT	OR	ON	2007/07/20 20:04
S21 4	27204	S212 S213	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 20:04
S21 2	3143	S209 with (((cost effective) near7 (design circuit element unit device module)) (short\$4 near2 (path route)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 20:04
S20 4	1260	(S178 with S179) and S181	US-PGPUB; USPAT	OR	ON	2007/07/20 20:04
S21 3	25265	S209 with ((cost importance value weight significan\$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 20:03
S19 9	18623	S179 with ((cost importance value weight significan\$4 new) near7 (design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2007/07/20 20:03
S18 8	2025	S179 with (((cost effective) near7 (design circuit element unit device module)) (short\$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2007/07/20 20:01
S21 1	1268	(S208 with S209) and S210	US-PGPUB; USPAT	OR	ON	2007/07/20 19:59
S21 0	59621	(netlist net-list (net adj list) (description near2 language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 19:57
S20 9	997785	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 19:57
S18 1	39991	(netlist net-list (net adj list) (description near2 language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT	OR	ON	2007/07/20 19:57

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S17 9	555027	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/20 19:57
S20 8	34769	(requirement condition\$3 limit\$5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/20 19:56
S17 8	25914	(requirement condition\$3 limit\$5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/20 19:56
S20 7	28	S188 and S206	US-PGPUB; USPAT	OR	ON	2007/07/18 15:08
S20 6	1260	S204 or S205	US-PGPUB; USPAT	OR	ON	2007/07/18 15:07
S20 5	54	(S178 with S181) and S179	US-PGPUB; USPAT	OR	ON	2007/07/18 15:06
S20 3	25914	((S178 same S179) (S178 same S181)) and (S181 S179)	US-PGPUB; USPAT	OR	ON	2007/07/18 15:00
S20 1	106	S178 same S179 same S181	US-PGPUB; USPAT	OR	ON	2007/07/18 14:56
S13 8	1	S135 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/18 14:30
S20 0	193	S184 and S199	US-PGPUB; USPAT	OR	ON	2007/07/18 14:29
S19 5	28	S184 and S188	US-PGPUB; USPAT	OR	ON	2007/07/18 14:04
S14 9	195	S136 same (cost importance value weight significan\$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/18 14:01
S18 4	1260	S183 and S181	US-PGPUB; USPAT	OR	ON	2007/07/18 13:58
S14 1	1	S140 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/18 13:58
S18 6	6072	S179 same (((cost effective) near7 (design circuit element unit device module)) (short\$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2007/07/18 13:30
S18 7	75	S185 and S186	US-PGPUB; USPAT	OR	ON	2007/07/18 13:14

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S18 5	428	S184 and (((cost effective) near7 (design circuit element unit device module))) (short\$4 near2 (path route)))	US-PGPUB; USPAT	OR	ON	2007/07/18 13:11
S18 3	25914	S178 with S179	US-PGPUB; USPAT	OR	ON	2007/07/18 13:10
S17 7	7	S176 and (("714"/\$.ccls.) ("324"/\$. ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S17 6	50	S174 and (S128 same (cost importance weight significan\$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S17 0	22	S169 and (("714"/\$.ccls.) ("324"/\$. ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S16 9	197	S166 and (S128 same (cost importance value weight significan\$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:35
S17 5	204	S174 and (cost importance weight significan\$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34
S17 4	269	S161 and S173	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34
S16 8	283	S166 and (cost importance value weight significan\$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34
S16 6	299	S161 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:34
S17 3	219930	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) with (cost importance (new near2 design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:33
S12 8	257750	(TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) with (cost importance value (new near2 design crcuit element unit device module))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:33
S15 9	841	S139 and (cost importance value weight significan\$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:32
S16 7	1	S166 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/17 19:28

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S15 1	12	S150 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:28
S16 2	107	S161 same S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:27
S16 0	525	S139 and (S128 same (cost importance value weight significan\$4 (new near2 (design crcuit element unit device module))))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:27
S16 1	434	S134 with S124	US-PGPUB; USPAT	OR	ON	2007/07/17 19:25
S13 6	381	S135 same S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:25
S13 5	1218	S134 same S124	US-PGPUB; USPAT	OR	ON	2007/07/17 19:25
S14 8	374	S136 and (cost importance value weight significan\$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:23
S15 2	0	S149 and "20040073859".pn.	US-PGPUB; USPAT	OR	ON	2007/07/17 19:22
S15 3	374	S136 and (cost importance value weight significan\$4 (new near2 (design crcuit element unit device module)))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:21
S15 0	127	S149 and @ad<="20021009"	US-PGPUB; USPAT	OR	ON	2007/07/17 19:20
S14 0	87	S139 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:20
S13 7	29	S136 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 19:17
S13 9	874	S135 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 19:00
S13 3	29	S132 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:58
S13 2	381	S130 same S128	US-PGPUB; USPAT	OR	ON	2007/07/17 18:58
S13 0	1218	S122 same S124	US-PGPUB; USPAT	OR	ON	2007/07/17 18:58
S13 4	4796822	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog (funcational adj configuration))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:57
S12 2	4796822	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog)	US-PGPUB; USPAT	OR	ON	2007/07/17 18:57

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S13 1	874	S130 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 18:54
S12 9	17422	S125 and S128	US-PGPUB; USPAT	OR	ON	2007/07/17 18:54
S12 5	25629	S122 and S124	US-PGPUB; USPAT	OR	ON	2007/07/17 18:54
S12 4	25914	(requirement condition\$3 limit\$5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:49
S12 3	41405	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:45
S97	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/17 18:44
S12 1	4	S120 and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S12 0	9	(test adj equipment adj platform)	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S11 8	871	S117 and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S11 7	1256	TEP (test adj equipment adj platform)	US-PGPUB; USPAT	OR	ON	2007/07/16 20:19
S11 9	8	S118 and ("714"/\$.ccls.)	US-PGPUB; USPAT	OR	ON	2007/07/16 20:17
S11 2	880	(environment near3 (operation operating operational normal functional)) and ((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data))) and ("714"/\$.ccls.) and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 20:17
S11 6	36	S113 and S99	US-PGPUB; USPAT	OR	ON	2007/07/16 20:16
S11 5	98	S113 and S98	US-PGPUB; USPAT	OR	ON	2007/07/16 19:48

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S11 3	161	S112 and S97	US-PGPUB; USPAT	OR	ON	2007/07/16 19:48
S11 1	1842	(environment near3 (operation operating operational normal funcational)) and ("714"/\$.ccls.) and (@ad<="20021009")	US-PGPUB; USPAT	OR	ON	2007/07/16 19:46
S10 8	6	("6256759" "6385750" "5450414" "5592493" "6708305" "5831996").pn.	US-PGPUB; USPAT	OR	ON	2007/07/16 19:43
S11 0	28831	(test\$4 verif\$4) with (new\$2 additional added) with (unit chip device module circuit)	US-PGPUB; USPAT	OR	ON	2007/07/16 18:52
S10 9	1	"20020124217".pn.	US-PGPUB; USPAT	OR	ON	2007/07/16 18:48
S10 7	58	S103 and ((test\$4 verif\$4 validat\$4 cost magnitude) near2 (cost weight priority control\$3 observation))	US-PGPUB; USPAT	OR	ON	2007/07/16 18:39
S10 5	47	S103 and ((test\$4 verif\$4 validat\$4) near2 (cost weight priority control\$3 observation))	US-PGPUB; USPAT	OR	ON	2007/07/16 17:30
S10 6	375	S103 and (cost priority control\$3 observation)	US-PGPUB; USPAT	OR	ON	2007/07/16 17:28
S10 4	375	S103 and (cost weight priority control\$3 observation)	US-PGPUB; USPAT	OR	ON	2007/07/16 17:28
S10 3	389	S102 and S101	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S10 2	16989	S97 and S98	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S10 1	389	(function program algorithm equation code) same S100	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S10 0	1216	S97 same S99	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S99	25887	(requirement condition\$3 limit\$5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S98	119899	(requirement condition\$3 limit\$5 criteria rule) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 17:20
S96	79	validation adj cost	US-PGPUB; USPAT	OR	ON	2007/07/16 17:19
S95	375	S94 and (cost weight priority control\$3 observation)	US-PGPUB; USPAT	OR	ON	2007/07/16 17:19

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S94	389	S93 and S81	US-PGPUB; USPAT	OR	ON	2007/07/16 15:53
S93	16989	S68 and S69	US-PGPUB; USPAT	OR	ON	2007/07/16 15:47
S71	7309	S68 same S69	US-PGPUB; USPAT	OR	ON	2007/07/16 15:47
S92	29	S89 and priority	US-PGPUB; USPAT	OR	ON	2007/07/16 15:38
S89	198	S88 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/16 15:38
S91	21	S71 and S89	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S90	81	S68 and S89	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S88	267	714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S87	27	(S71) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S78	224	S77 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/16 14:42
S86	3	(S81 S77) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 13:33
S85	3	(S82 S79) and 714/741.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/16 13:31
S84	14	(S82 S79) and simulation	US-PGPUB; USPAT	OR	ON	2007/07/16 13:29
S83	16	S82 not S79	US-PGPUB; USPAT	OR	ON	2007/07/16 13:13
S82	35	S81 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/16 13:01
S79	19	S77 and (("714"/\$.ccls.) ("324"/\$.ccls.))	US-PGPUB; USPAT	OR	ON	2007/07/16 13:01
S81	389	(function program algorithm equation code) same S76	US-PGPUB; USPAT	OR	ON	2007/07/16 13:00
S77	311	(function program algorithm) same S76	US-PGPUB; USPAT	OR	ON	2007/07/16 12:59
S80	14	("4204633" "4779273" "5390193" "5657240" "5726996" "5831996" "5862149" "5896401" "6215327" "6442722" "6463560" "6467058" "6477684" "6611933").pn.	US-PGPUB; USPAT	OR	ON	2007/07/16 12:53
S76	1216	S68 same S75	US-PGPUB; USPAT	OR	ON	2007/07/16 10:41
S73	2479	(function program algorithm) same S71	US-PGPUB; USPAT	OR	ON	2007/07/16 10:41

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S75	25887	(requirement condition\$3 limit\$5 criteria rule) near5 (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:40
S74	52106	(requirement condition\$3 limit\$5 criteria rule) with (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:40
S69	119899	(requirement condition\$3 limit\$5 criteria rule) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:40
S68	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:27
S67	24	S49 same ((critical adj point) (observation adj point) ((target specific particular) near2 (circuit chip module device unit apparatus)))	US-PGPUB; USPAT	OR	ON	2007/07/16 10:25
S65	327	S49 and ((critical adj point) (observation adj point) ((target specific particular) near2 (circuit chip module device unit apparatus)))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:45
S66	262	S65 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:33
S64	437	S49 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:33
S61	257	S60 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:31
S63	1	"20050149804".pn.	US-PGPUB; USPAT	OR	ON	2007/07/12 18:26
S39	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:26



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S62	21	S49 same ((target specific particular) near2 (circuit chip module device unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:21
S60	320	S49 and ((target specific particular) near2 (circuit chip module device unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:21
S50	11135	(test\$4 verif\$4) with ((target specific particular) near2 (circuit chip module device unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:20
S51	111	S49 and S50	US-PGPUB; USPAT	OR	ON	2007/07/12 18:17
S54	34	S53 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 18:08
S53	47	S49 and (BDD (binary near2 (decision determination determin\$3 solution)) (state adj diagram))	US-PGPUB; USPAT	OR	ON	2007/07/12 18:08
S49	552	(netlist net-list (net adj list) (description adj language) HDL HTL RTL VHDL verilog) with ((test adj (pattern sequence vector)) (((input near2 output) input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 17:48
S52	84	S51 and @ad<="20031008"	US-PGPUB; USPAT	OR	ON	2007/07/12 16:47
S48	36	S47 same ((target specific particular) near2 (circuit chip module device unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:45
S47	1643	(netlist net-list (net adj list) (description adj language) HDL HTL RTL VHDL verilog) with ((test adj (pattern sequence vector data)) (((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:43
S44	120	S42 same ((target specific particular) near2 (circuit chip module device unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:16
S42	12632	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) with ((test adj (pattern sequence vector data)) ((input output input/output output/input) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:15
S46	38	S44 and priority	US-PGPUB; USPAT	OR	ON	2007/07/12 16:13
S45	0	S44 same priority	US-PGPUB; USPAT	OR	ON	2007/07/12 16:13

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S43	3379	S42 and ((target specific particular) near2 (circuit chip module device unit apparatus))	US-PGPUB; USPAT	OR	ON	2007/07/12 16:13
S41	988	S39 same ((condition limit\$4 constraint paramiter provision circumstances circumstanc requirement\$4 limitation restraint) near5 (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data))))	US-PGPUB; USPAT	OR	ON	2007/07/12 14:39
S40	4352	S39 and ((condition limit\$4 constraint paramiter provision circumstances circumstanc requirement\$4 limitation restraint) near5 (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data))))	US-PGPUB; USPAT	OR	ON	2007/07/12 14:39
S35	41369	(netlist net-list (net adj list) (description language) HDL HTL RTL VHDL verilog) same (TP (test adj (pattern sequence vector data)) ((input output) near2 (pattern sequence vector data)))	US-PGPUB; USPAT	OR	ON	2007/07/12 14:28
S38	1	"5542043".pn.	US-PGPUB; USPAT	OR	ON	2007/07/12 14:15
S37	1	"5870590".pn.	US-PGPUB; USPAT	OR	ON	2007/07/12 14:15
S36	12310	S35 and (binary)	US-PGPUB; USPAT	OR	ON	2007/07/12 14:15
S2	1	"20040073859"	US-PGPUB; USPAT	OR	ON	2007/07/12 13:56
S34	5	S30 and ((test test\$4 verify verif\$4) near2 (pattern sequence vector))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:55
S32	159	(validate validating validation) and (functional near2 device) and ((test test\$4 verify verif\$4) near2 (pattern sequence vector))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:55
S30	30	((functional configuration) near2 (information data)) same (condition)) and (validation near2 (item unit circuit module chip device))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:55
S33	41	S32 and ((resource device circuit element) near2 (constraint restriction limitation constrain\$4 restrict\$4 limit\$4))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:53
S31	698	((functional configuration) near2 (information data)) and (condition)) and (validation near2 (item unit circuit module chip device))	US-PGPUB; USPAT	OR	ON	2007/07/06 15:40

## EAST Search History

S29	149990	(configuration same condition)	US-PGPUB; USPAT	OR	ON	2007/07/06 15:15
S5	2	("5542043" "5870590").pn.	USPAT	OR	ON	2007/07/06 15:11